

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO.

09/819762

CONT/PRIOR

7

CLASS

~~438~~

SUBCLASS

792

ART UNIT

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EXAMINER

Barnister

TITLE	APPLICANTS
1. Chairman	1. Chairman
2. Vice Chairman	2. Vice Chairman
3. Secretary	3. Secretary
4. Treasurer	4. Treasurer
5. Member	5. Member
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99. Member	99. Member
100. Member	100. Member

• **Yoshiaki Sakuma**

Heterobipolar transistor and a method of forming a SiGeC mixed crystal layer

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			ISSUE FEE	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Legal Instruments Examiner) (Date)			Amount Due	Date Paid
				ISSUE BATCH NUMBER	

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